

PRELIMINARY

Data Sheet

April 5, 2004

FN7382.2

670MHz Low Noise Amplifiers



The EL5132 and EL5133 are ultra-low voltage noise, high speed voltage feedback amplifiers that are ideal for

applications requiring low voltage noise, including communications and imaging. These devices offer extremely low power consumption for exceptional noise performance. Stable at gains as low as 10, these devices offer 120mA of drive performance. Not only do these devices find perfect application in high gain applications, they maintain their performance down to lower gain settings.

These amplifiers are available in small package options (SOT-23) as well as the industry-standard SO packages. All parts are specified for operation over the -40°C to +85°C temperature range.

Ordering Information

PART NUMBER	PACKAGE	TAPE & REEL	PKG. DWG. #
EL5132IS	8-Pin SO	-	MDP0027
EL5132IS-T7	8-Pin SO	7"	MDP0027
EL5132IS-T13	8-Pin SO	13"	MDP0027
EL5133IW-T7	5-Pin SOT-23	7" (3K pcs)	MDP0038
EL5133IW-T7A	5-Pin SOT-23	7" (250 pcs)	MDP0038

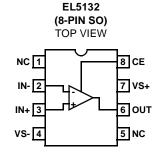
Features

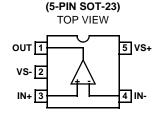
- · 670MHz -3dB bandwidth
- Ultra low noise 0.9nV/√Hz
- 1000V/µs slew rate
- Low supply current = 12mA
- Single supplies from 5V to 12V
- Dual supplies from ±2.5V to ±5V
- · Fast disable on the EL5132
- · Low cost

Applications

- Imaging
- Instrumentation
- · Communications devices

Pinouts





EL5133

EL5132, EL5133

Absolute Maximum Ratings (T_A = 25°C)

Supply Voltage from V _S + to V _S	Storage Temperature65°C to +125°C
I _{IN} -, I _{IN} +, CE	Ambient Operating Temperature
Continuous Output Current	Operating Junction Temperature
Power Dissipation See Curves	

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

$\textbf{Electrical Specifications} \qquad \text{V}_{S}\text{+} = +5\text{V}, \text{ V}_{S}\text{-} = -5\text{V}, \text{ R}_{L} = 150\Omega, \text{ R}_{F} = 900\Omega, \text{ R}_{G} = 100\Omega, \text{ T}_{A} = 25^{\circ}\text{C}, \text{ unless otherwise specified.}$

PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT
Vos	Offset Voltage		-1	0.5	1	mV
T _C V _{OS}	Offset Voltage Temperature Coefficient	Measured from T _{MIN} to T _{MAX}		0.8		μV/°C
IB	Input Bias Current	V _{IN} = 0V	8	12	20	μA
los	Input Offset Current	V _{IN} = 0V	-1250	400	+1250	nA
T _C l _{OS}	Input Bias Current Temperature Coefficient	Measured from T _{MIN} to T _{MAX}		3		nA/°C
PSRR	Power Supply Rejection Ratio	V _S + = ±4.75V to ±5.25V	75	87		dB
CMRR	Common Mode Rejection Ratio	V _{IN} = ±3.0 V	80	100		dB
CMIR	Common Mode Input Range	Guaranteed by CMRR test	±3	±3.3		V
R _{IN}	Input Resistance	Common mode	2	5		МΩ
C _{IN}	Input Capacitance			2		pF
IS	Supply Current		9.2	11	13	mA
AVOL	Open Loop Gain	V_{OUT} = ±2.5V, R_L = 1k Ω to GND	5	8.5		KV/V
Vo	Output Voltage Swing	$R_F = 900\Omega, R_G = 100\Omega, R_L = 150\Omega$	±3.1	3.5		V
I _{SC}	Short Circuit Current	$R_L = 10\Omega$	70	140		mA
BW	-3dB Bandwidth	$A_V = +10$, $R_L = 1k\Omega$		670		MHz
BW	±0.1dB Bandwidth	$A_V = +10$, $R_L = 1k\Omega$		90		MHz
GBWP	Gain Bandwidth Product			3000		MHz
PM	Phase Margin	$R_L = 1k\Omega$, $C_L = 6pF$		55		0
SR	Slew Rate	R _L = 100Ω, V _{OUT} = ±2.5V	700	1000		V/µs
t _R , t _F	Rise Time, Fall Time	±0.1V _{STEP}		TBD		ns
OS	Overshoot	±0.1V _{STEP}		TBD		%
t _{PD}	Propagation Delay	±0.1V _{STEP}		TBD		ns
ts	0.01% Settling Time			6.6		ns
dG	Differential Gain	$A_V = +2$, $R_F = 1k\Omega$		0.01		%
dP	Differential Phase	$A_V = +2$, $R_F = 1k\Omega$		0.01		0
e _N	Input Noise Voltage	f = 10kHz		0.9		nV/√Hz
i _N	Input Noise Current	f = 10kHz		4.9		pA/√Hz

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